

FIG. 1

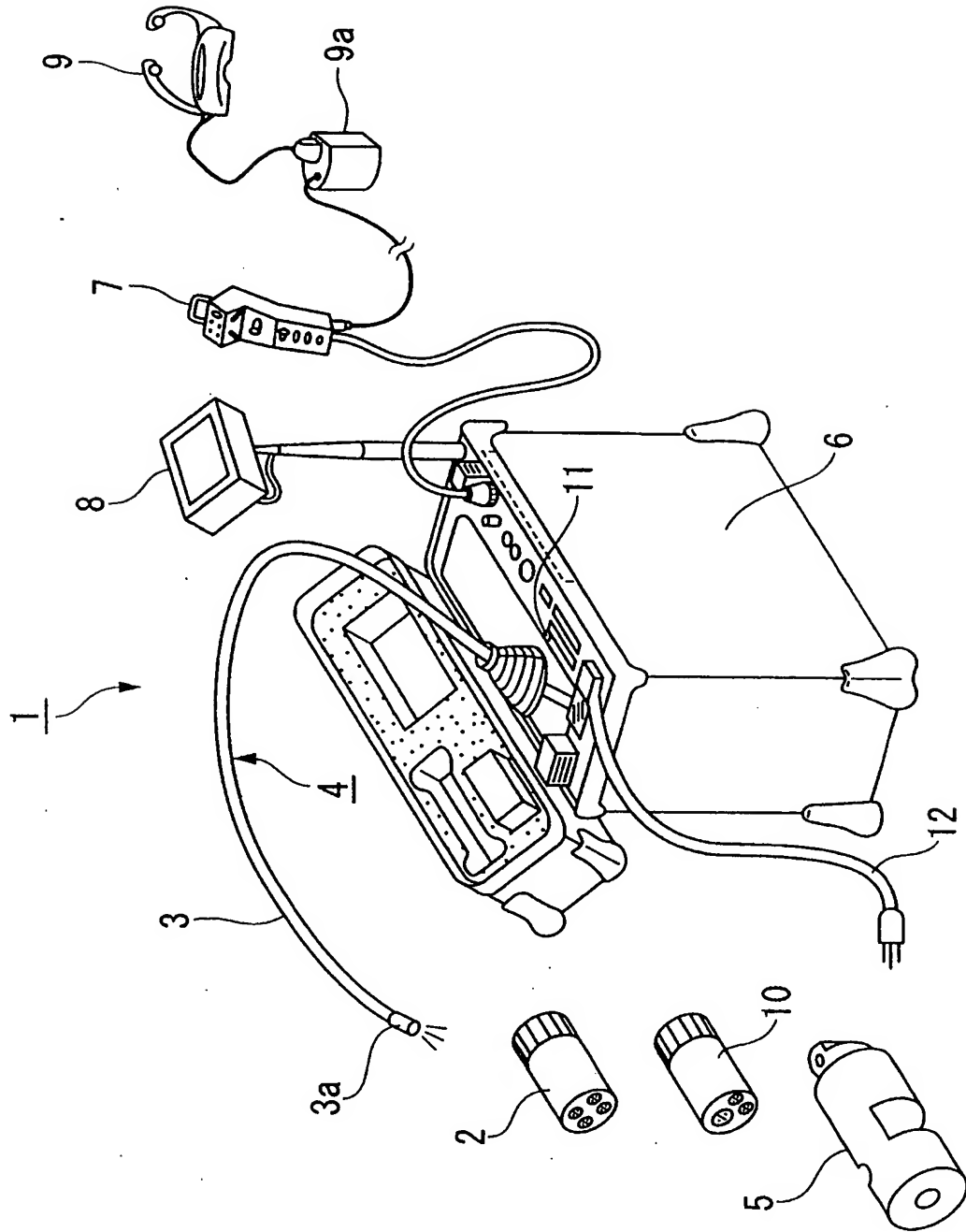


FIG. 2

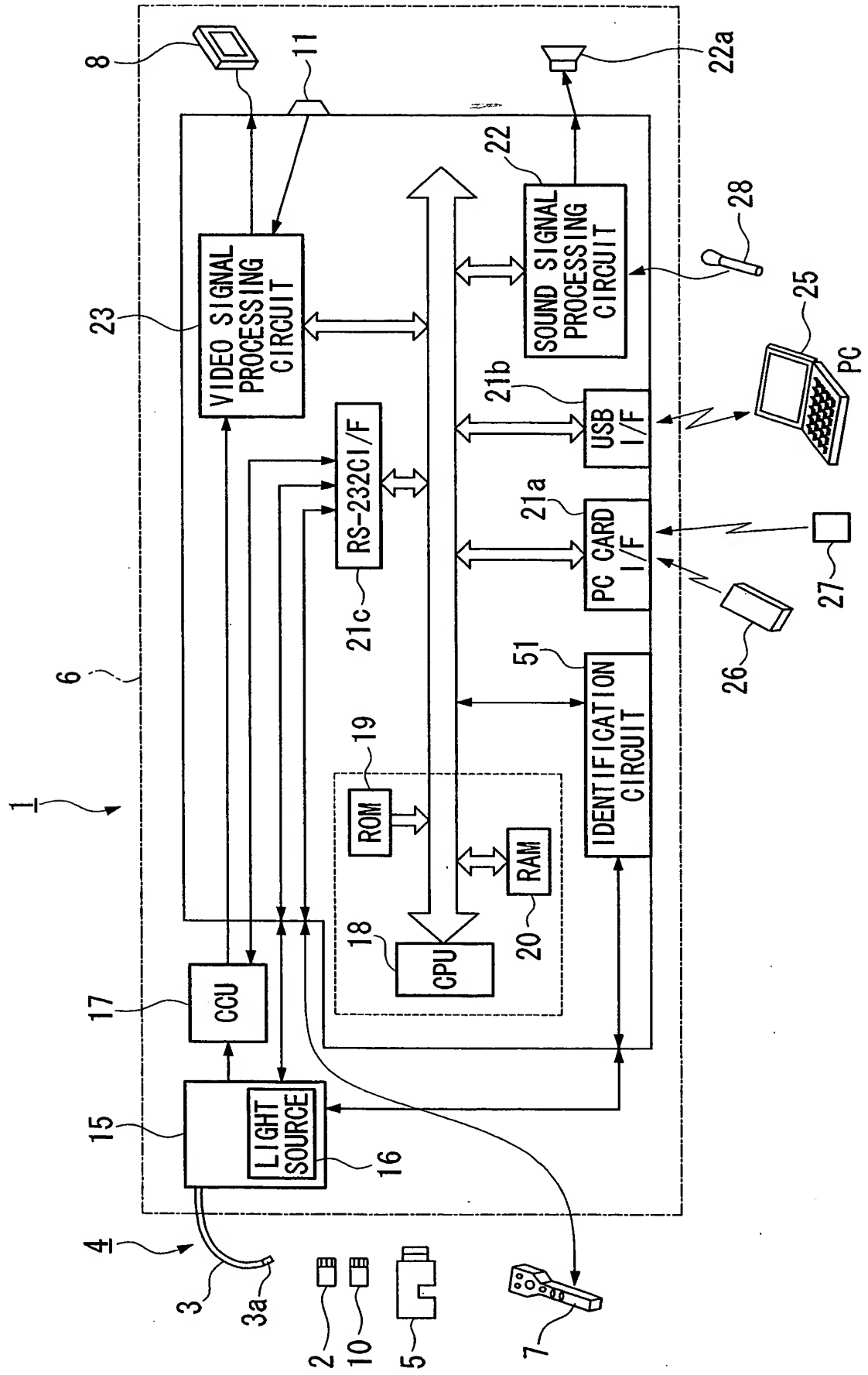


FIG. 3

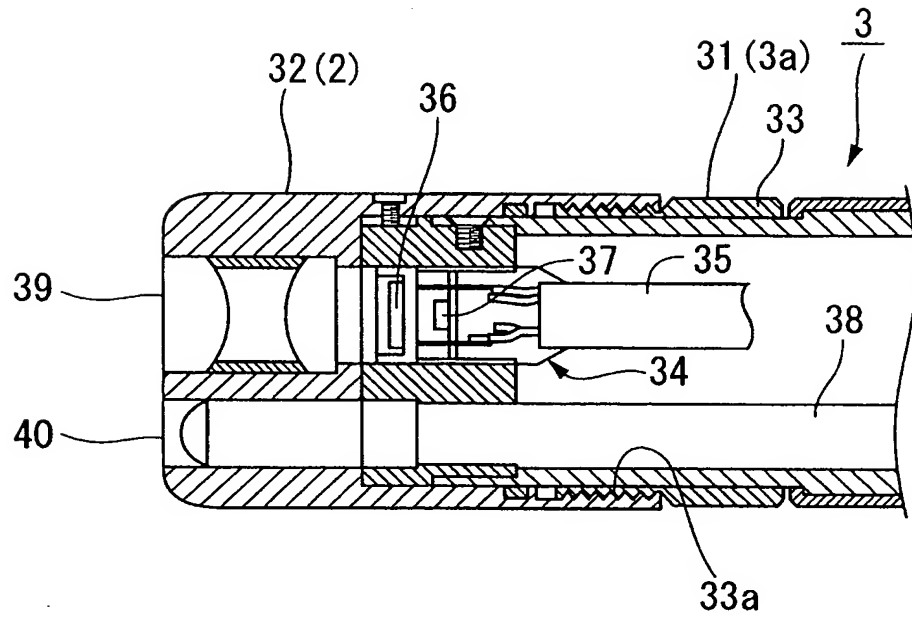


FIG. 4

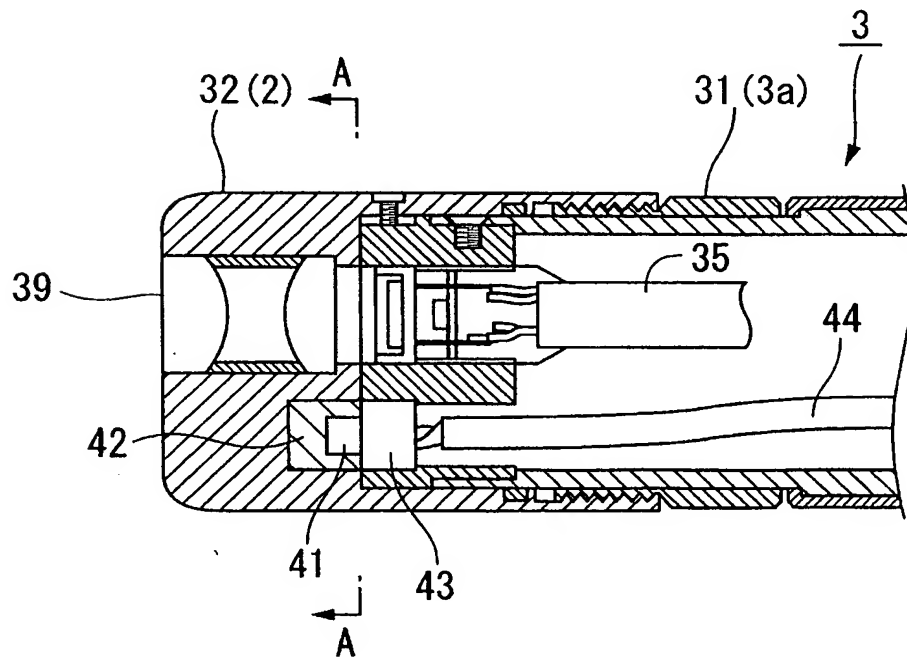


FIG. 5

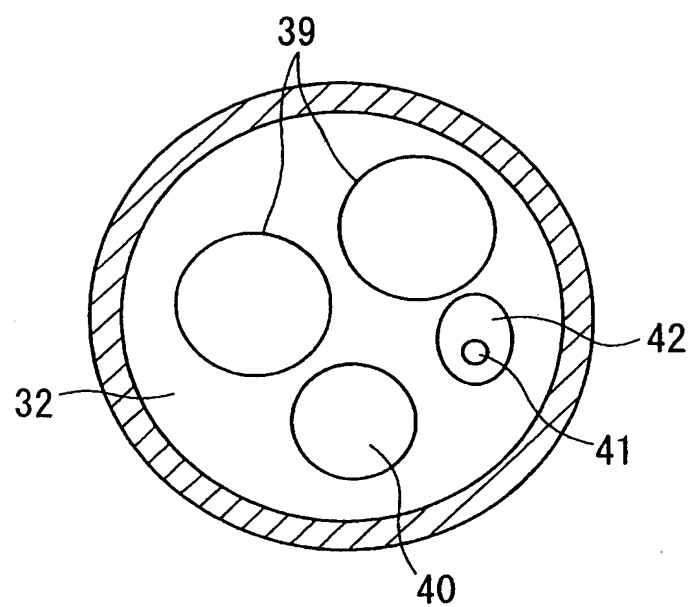


FIG. 6

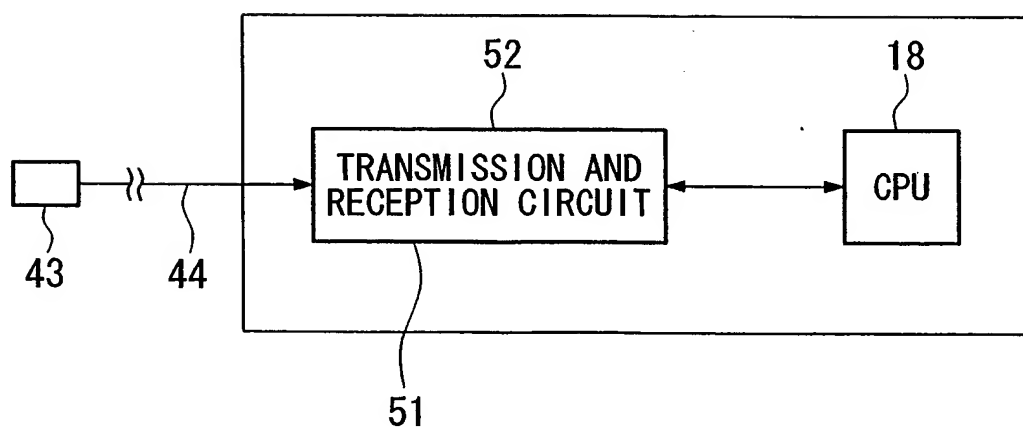


FIG. 7

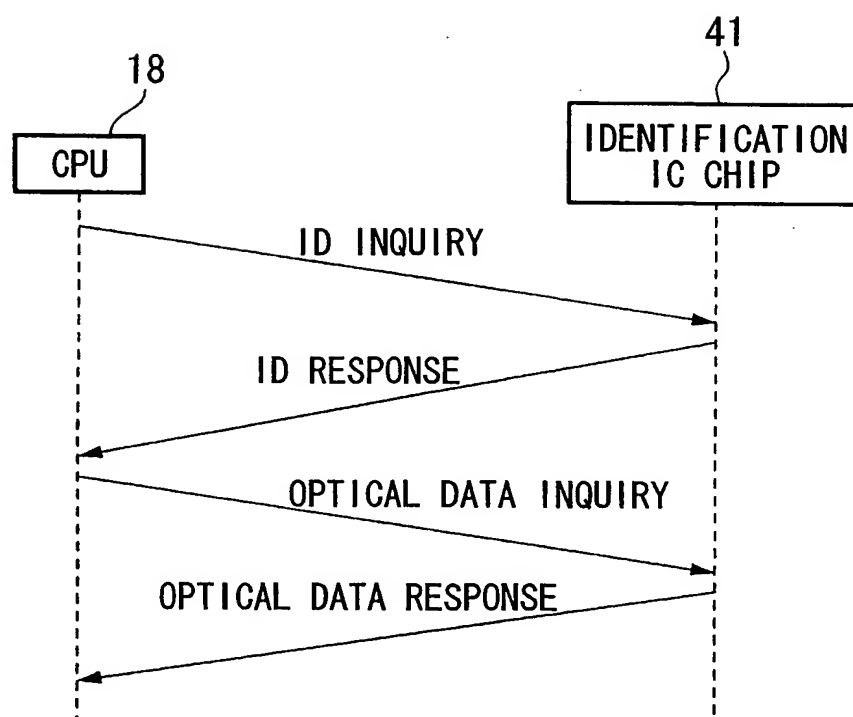


FIG. 8

ITEMS	DIRECTION	FORMAT
ID INQUIRY	CPU->IC	ID[EOF]
OPTICAL DATA INQUIRY	CPU->IC	DATA[EOF]
ID	IC->CPU	1234[EOF]
OPTICAL DATA	IC->CPU	120,320,240[EOF]

FIG. 9

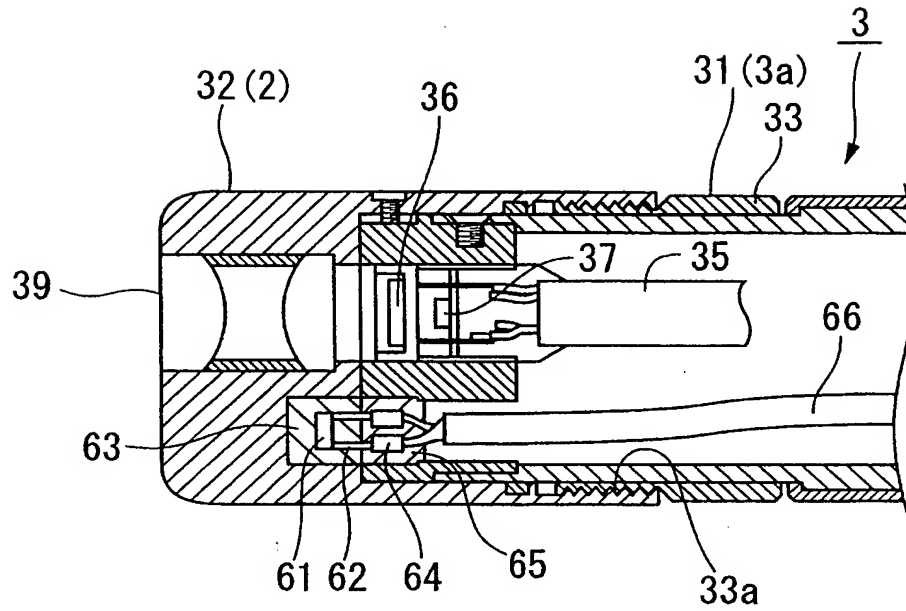


FIG. 10

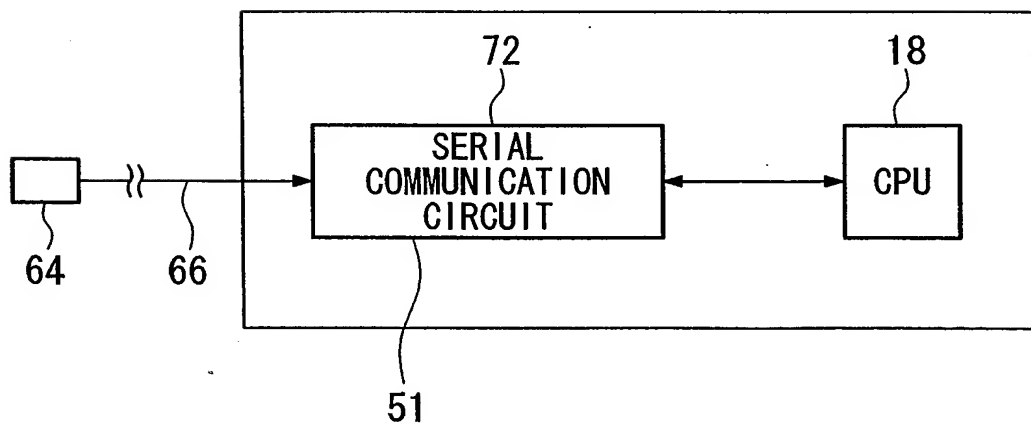


FIG. 11

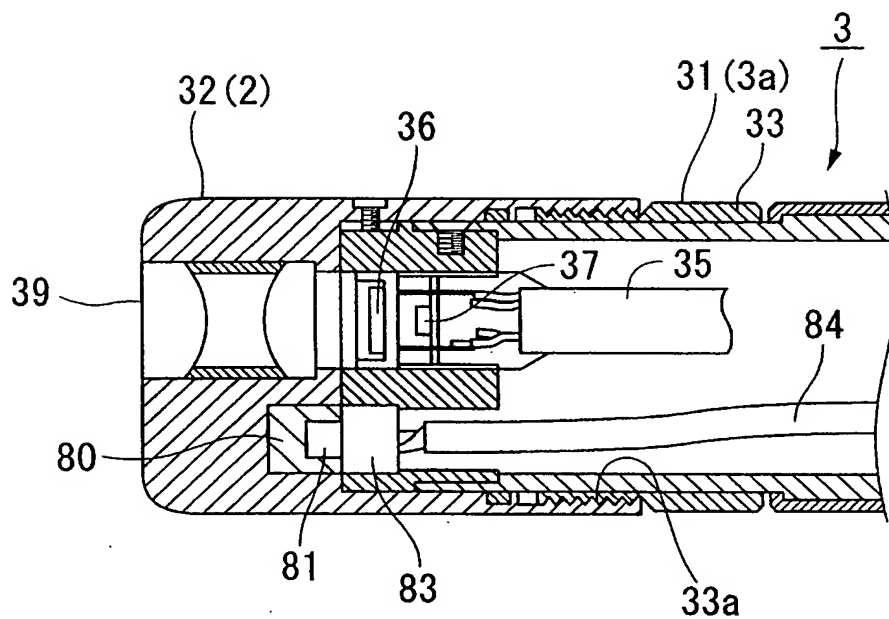


FIG. 12

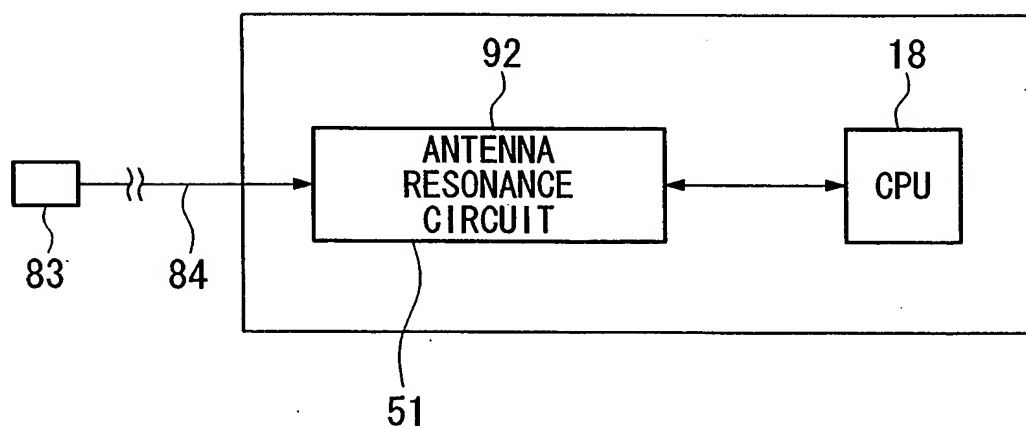


FIG. 13

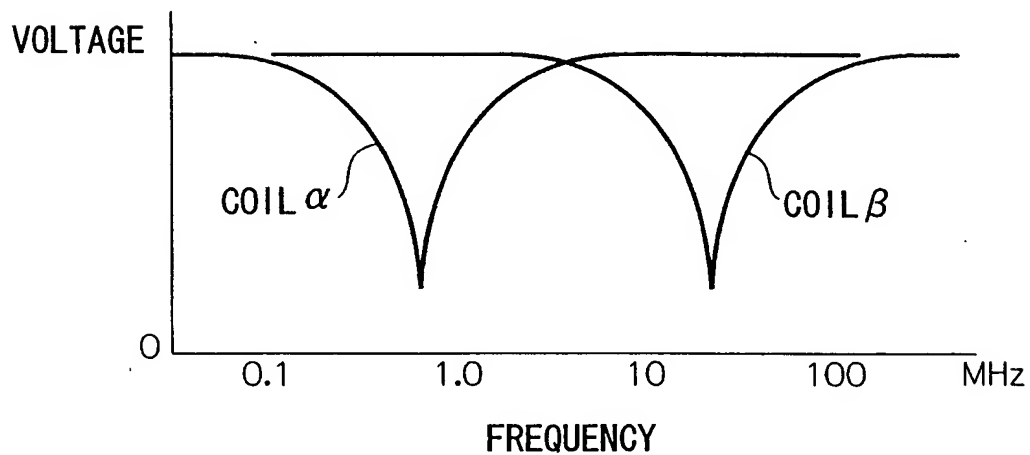


FIG. 14

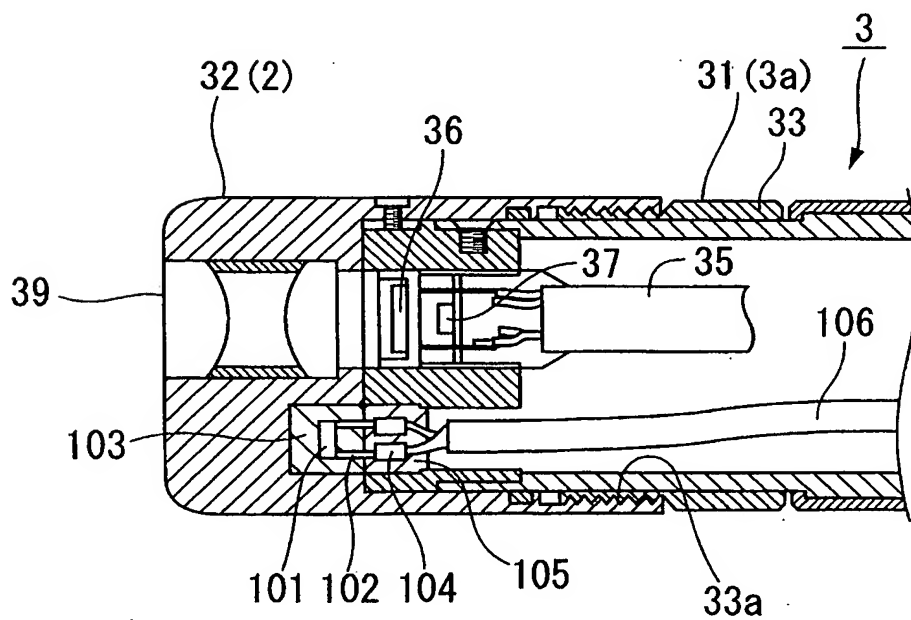




FIG. 15

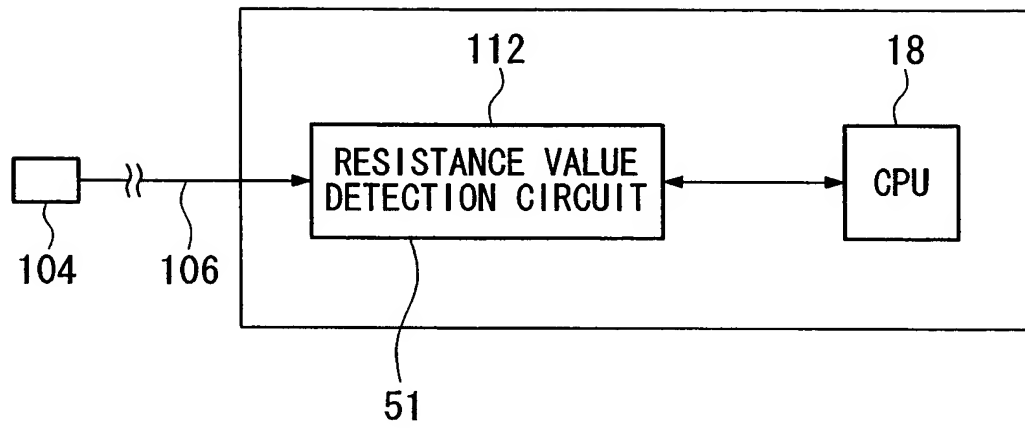


FIG. 16

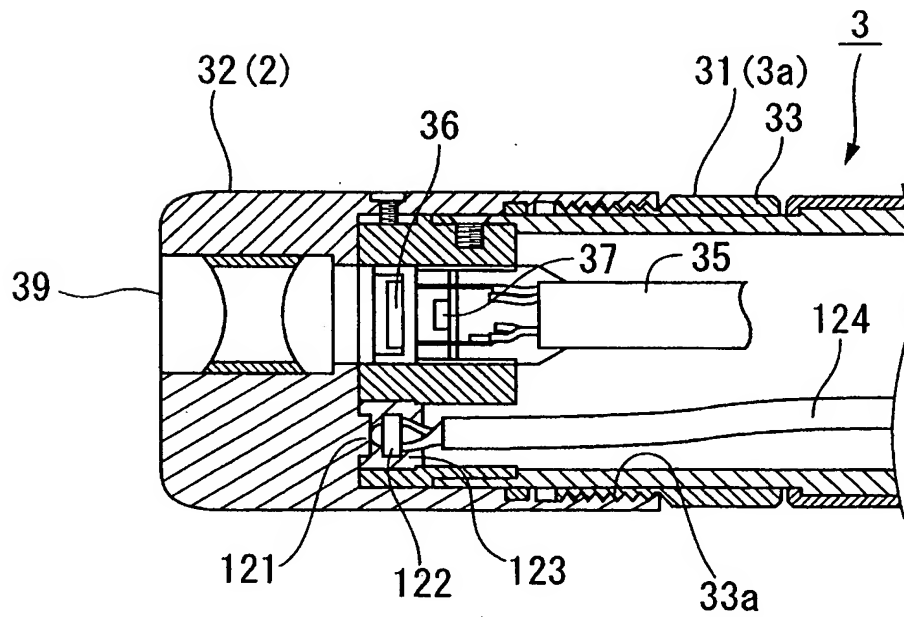


FIG. 17

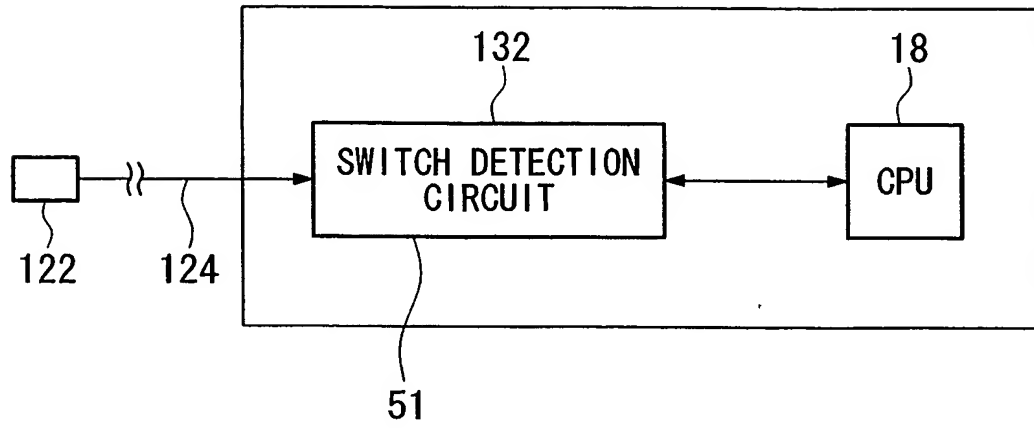


FIG. 18

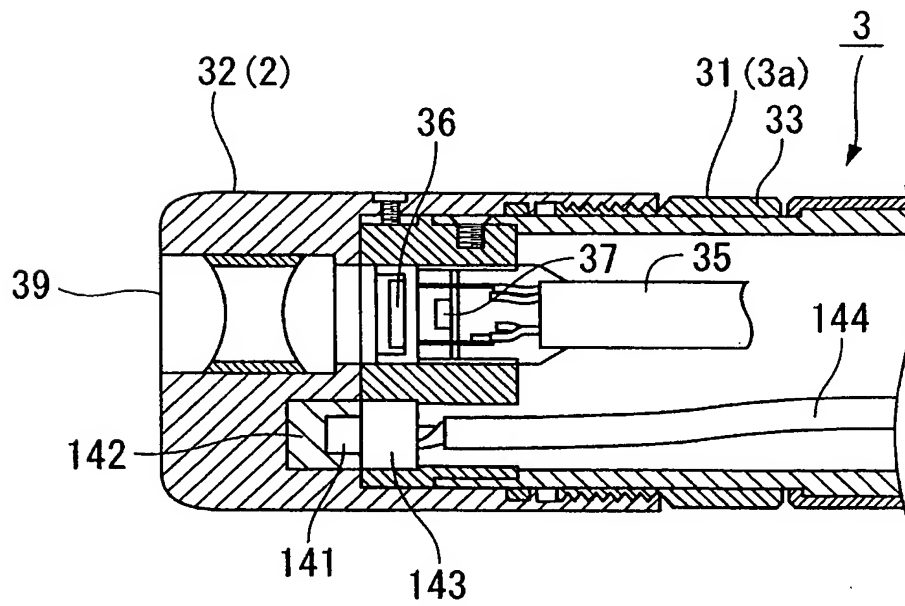


FIG. 19

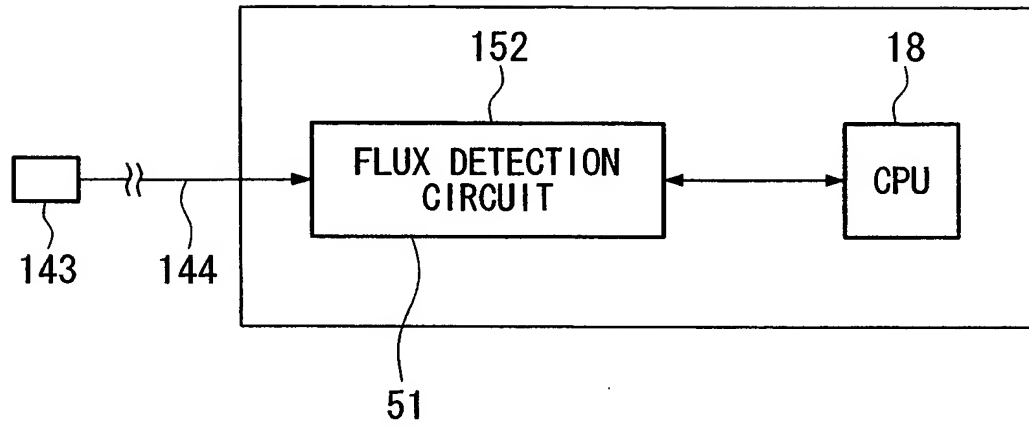


FIG. 20

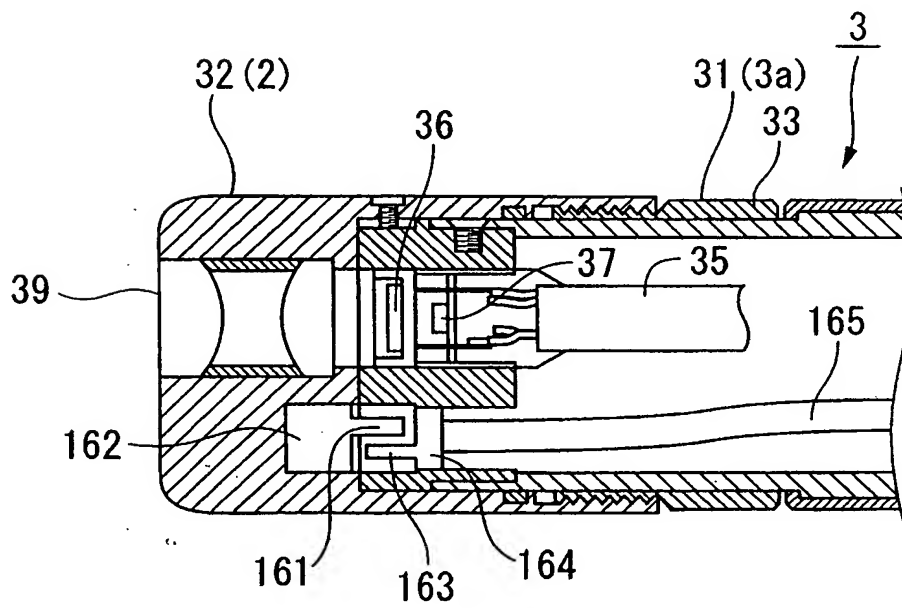
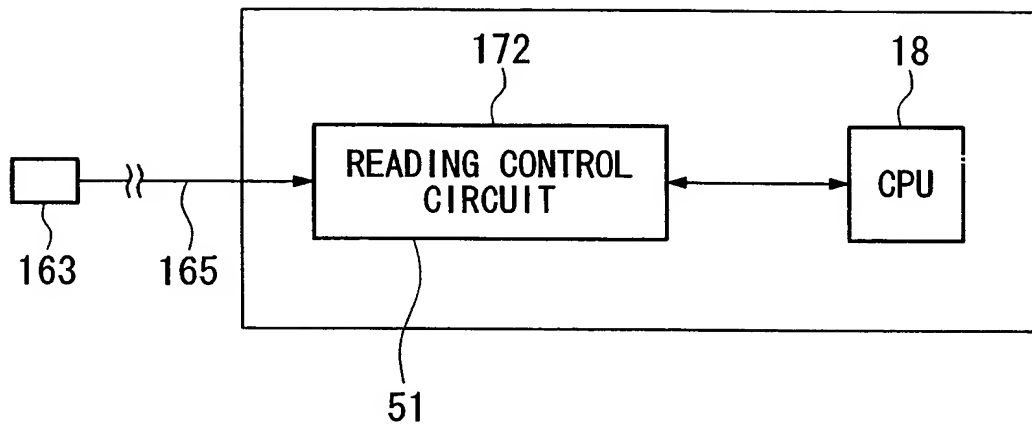


FIG. 21



The diagram illustrates a system 1, enclosed in a dashed box, designed for identification and data processing. The system is composed of several interconnected components:

- Input/Output and Imaging Components:**
  - 4:** A cable or connector leading into the system.
  - 3:** A component connected to the cable 4, which includes a sub-component **3a**.
  - 2:** A small rectangular component connected to the main system bus.
  - 10:** Another small rectangular component connected to the bus.
  - 5:** A component with a multi-pin connector, likely for a keyboard or mouse.
  - 7:** A handheld identification device (like a barcode scanner or RFID reader) that sends data to the **IDENTIFICATION SECTION 200**.
  - 8:** A camera or imaging sensor connected to the **VIDEO SIGNAL PROCESSING CIRCUIT 23**.
  - 11:** A speaker or output device connected to the video processing circuit.
- Core Processing and Control Components:**
  - 15:** A **LIGHT SOURCE** connected to the **CCU 17** and the **IDENTIFICATION SECTION 200**.
  - 17:** The **CCU** (Camera Control Unit) manages the light source and video input.
  - 18:** A dashed box containing the **CPU 18** and **RAM 20**, which are connected to the **ROM 19**.
  - 19:** **ROM** (Read-Only Memory) providing storage for the system.
  - 20:** **RAM** (Random Access Memory) for temporary data storage.
- Signal Processing and Communication Components:**
  - 23:** **VIDEO SIGNAL PROCESSING CIRCUIT** that receives input from the camera (8) and the CCU (17).
  - 22:** **SOUND SIGNAL PROCESSING CIRCUIT** connected to the main system bus.
  - 21a:** **PC CARD I/F** (Interface) for connecting to a PC card.
  - 21b:** **USB I/F** (Interface) for connecting to a USB device.
  - 21c:** **RS-232C I/F** (Interface) for serial communication.
  - 51:** An **IDENTIFICATION CIRCUIT** that receives data from the **IDENTIFICATION SECTION 200** and the **CPU 18**.
- External Connections:**
  - 25:** A **PC** (Personal Computer) connected via the USB I/F (21b).
  - 26:** A small external device connected via the PC CARD I/F (21a).
  - 27:** A small external device connected via the RS-232C I/F (21c).
  - 28:** A microphone connected to the sound signal processing circuit (22).
  - 22a:** A speaker or output device connected to the sound signal processing circuit (22).

The system is designed to handle identification data from various sources (7, 200), process video (23) and audio (22) signals, and manage data through multiple interfaces (21a, 21b, 21c) connected to external devices (25, 26, 27, 28).

FIG. 23

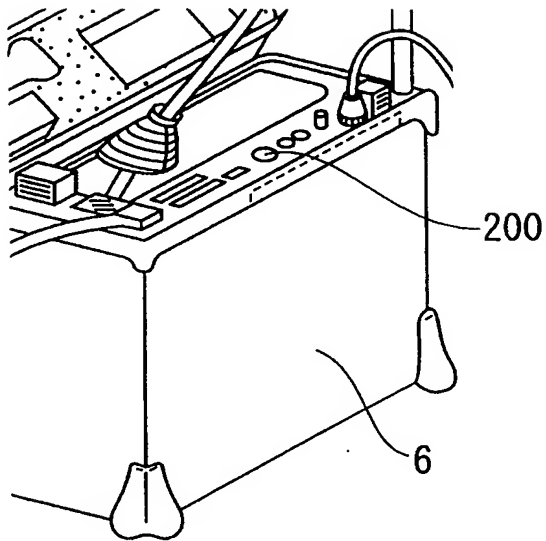


FIG. 24

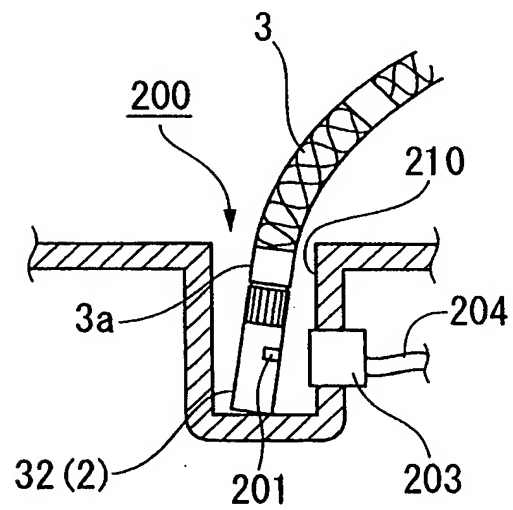


FIG. 25

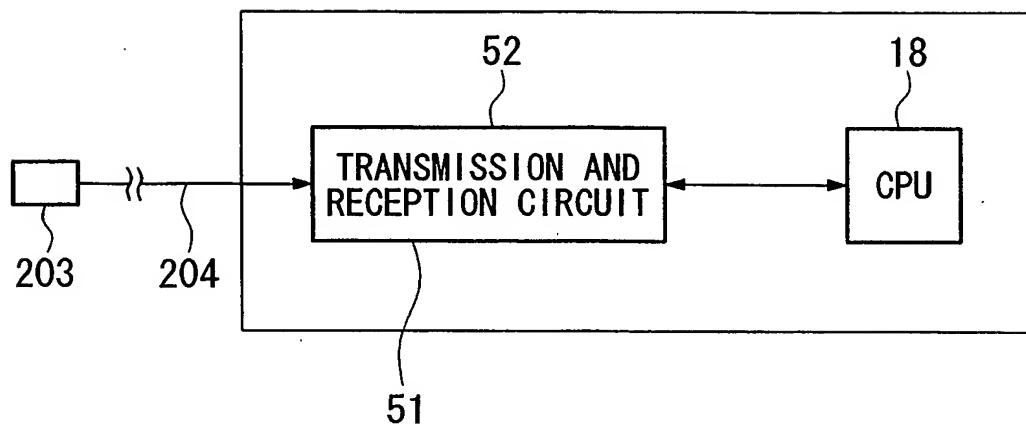


FIG. 26

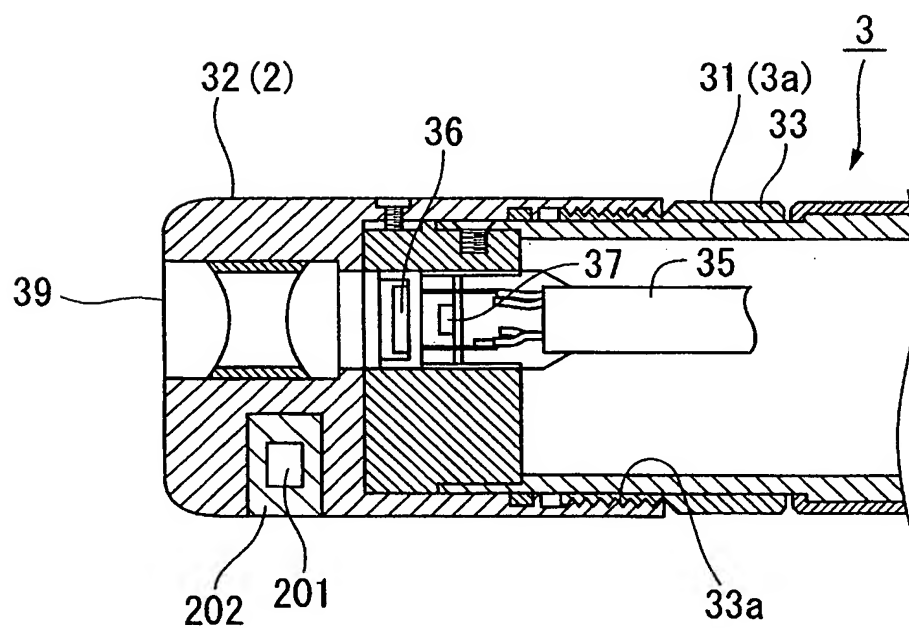


FIG. 27

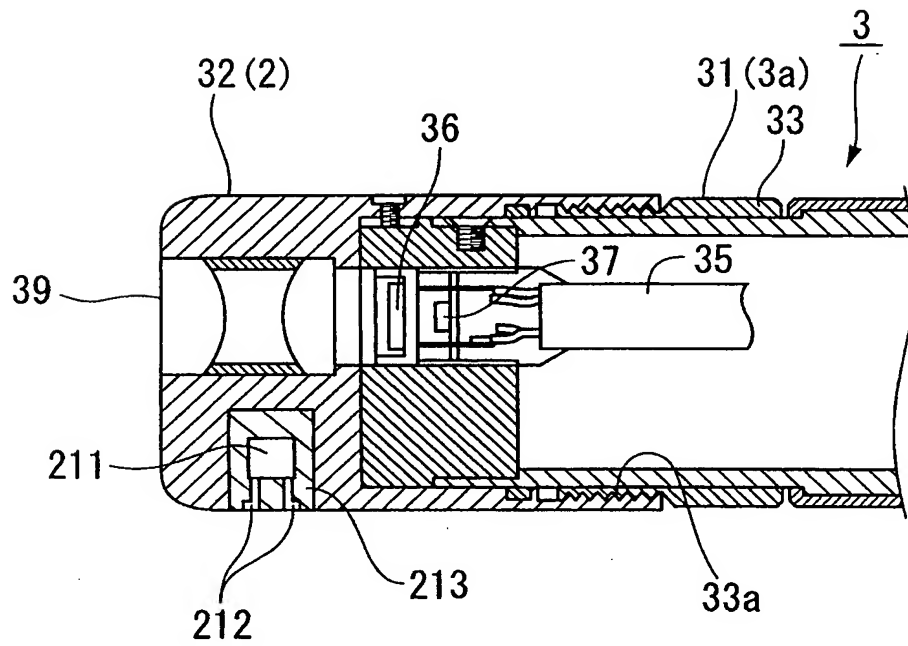


FIG. 28

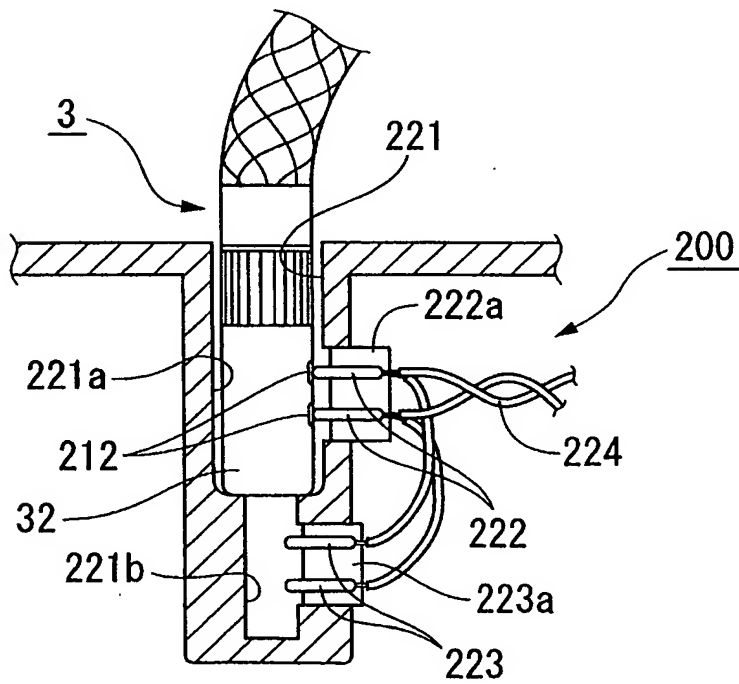




FIG. 29

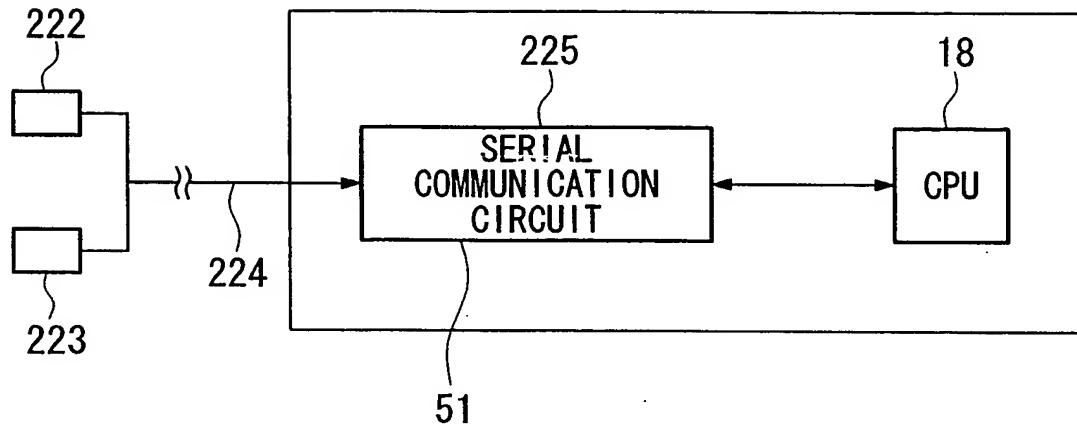


FIG. 30

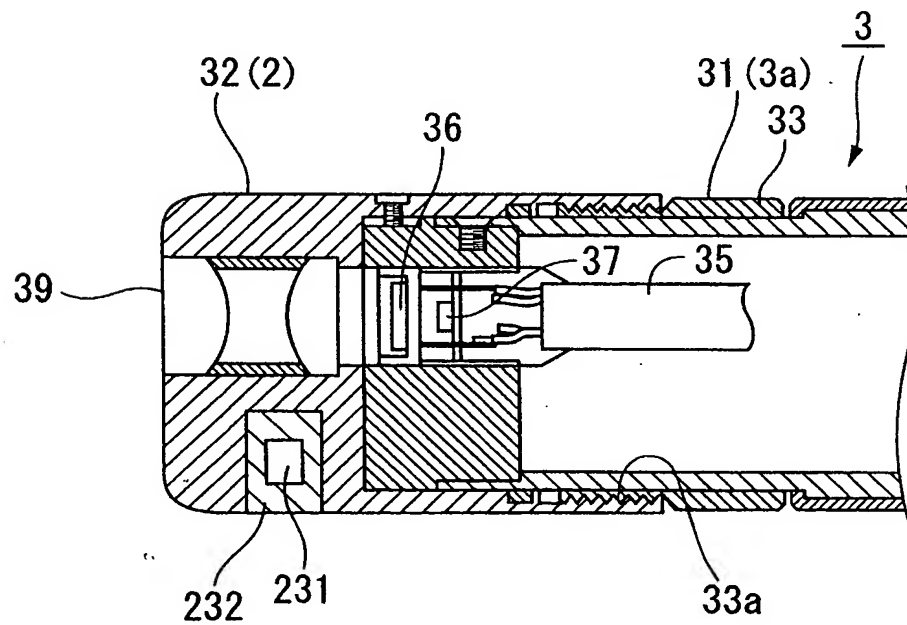


FIG. 31

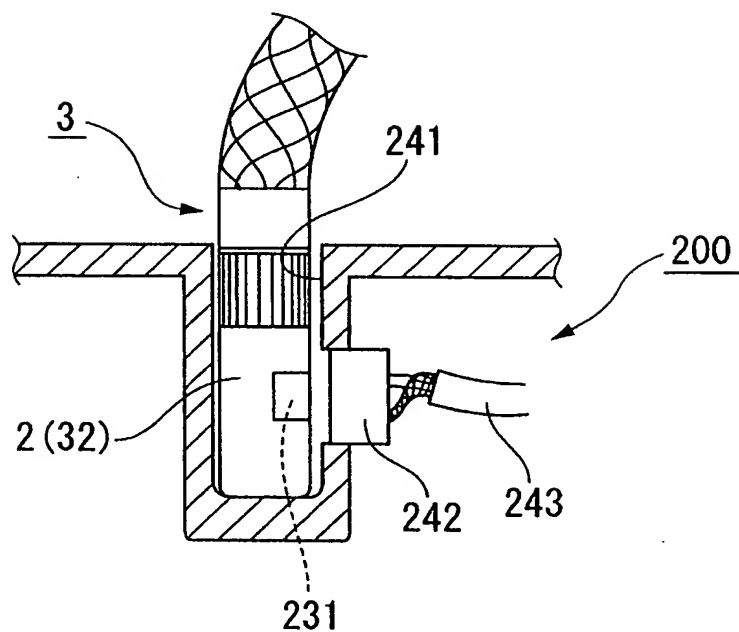


FIG. 32

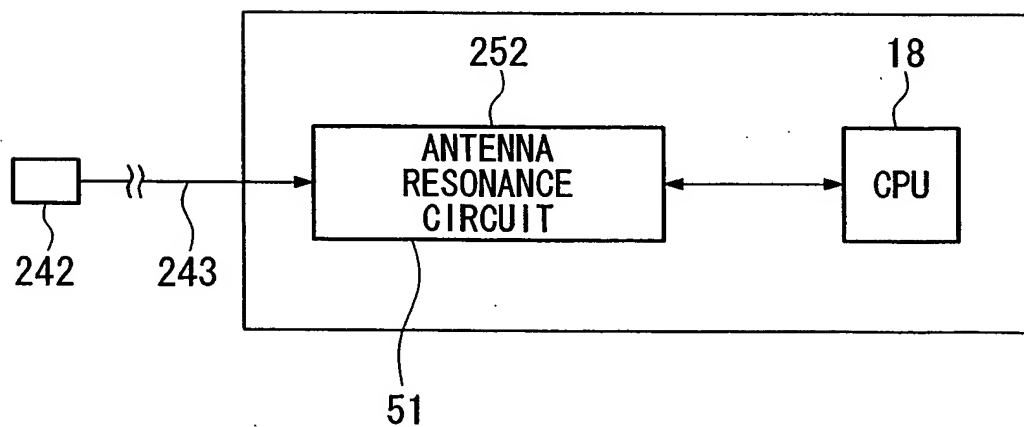


FIG. 33

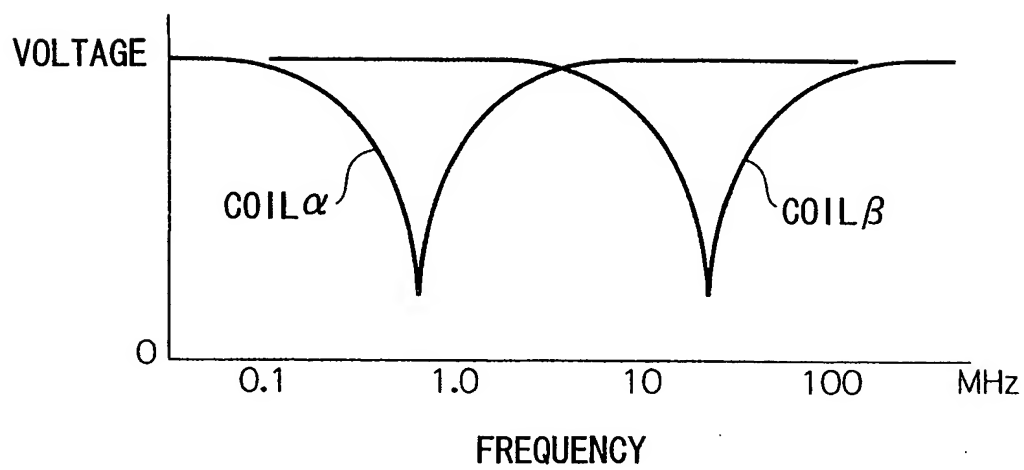


FIG. 34

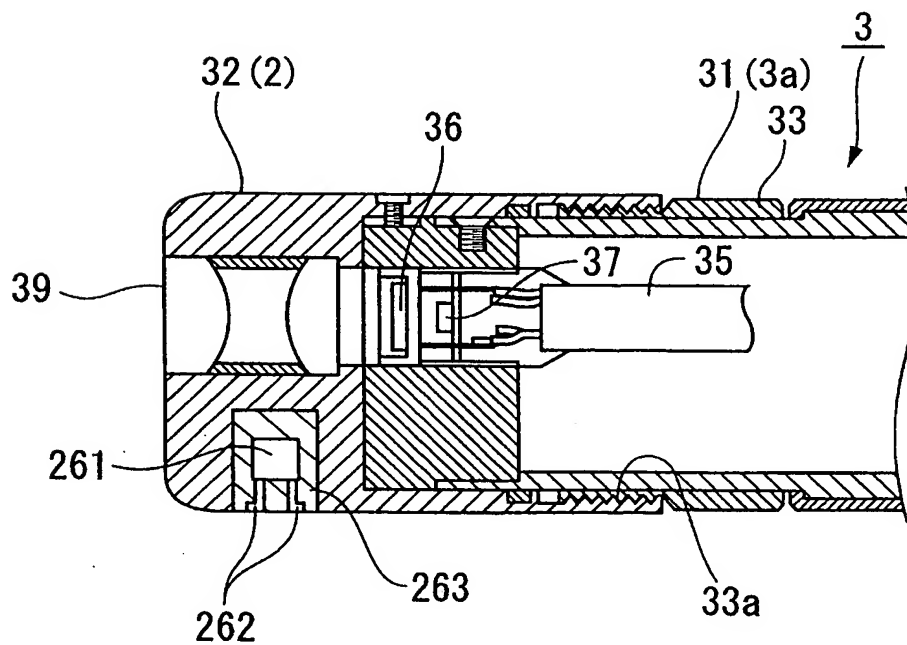


FIG. 35

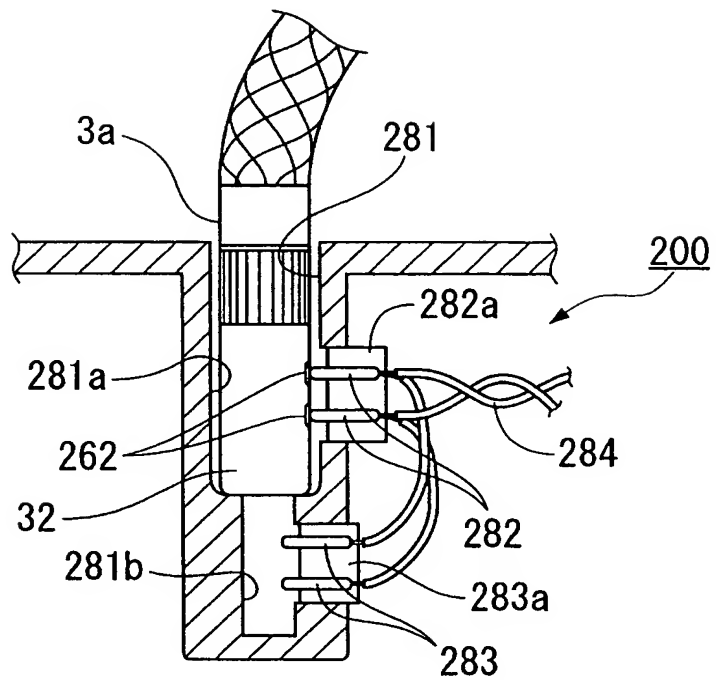


FIG. 36

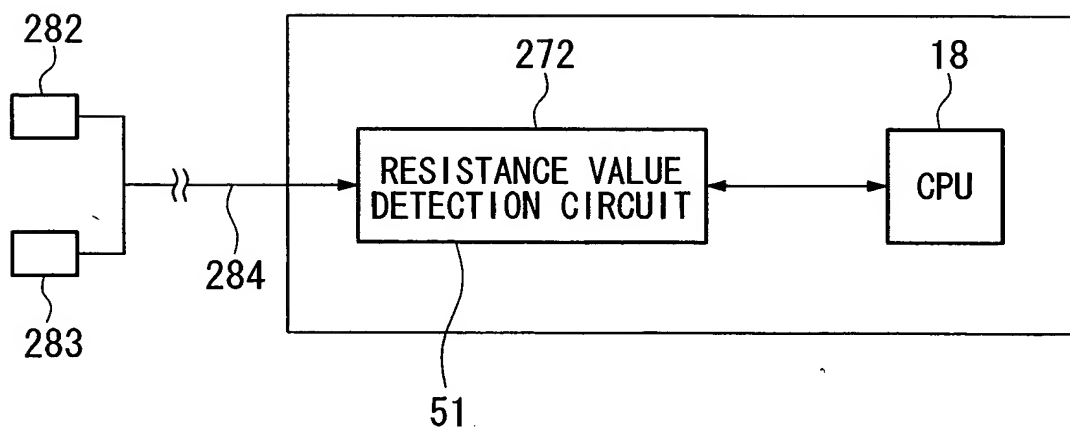


FIG. 37

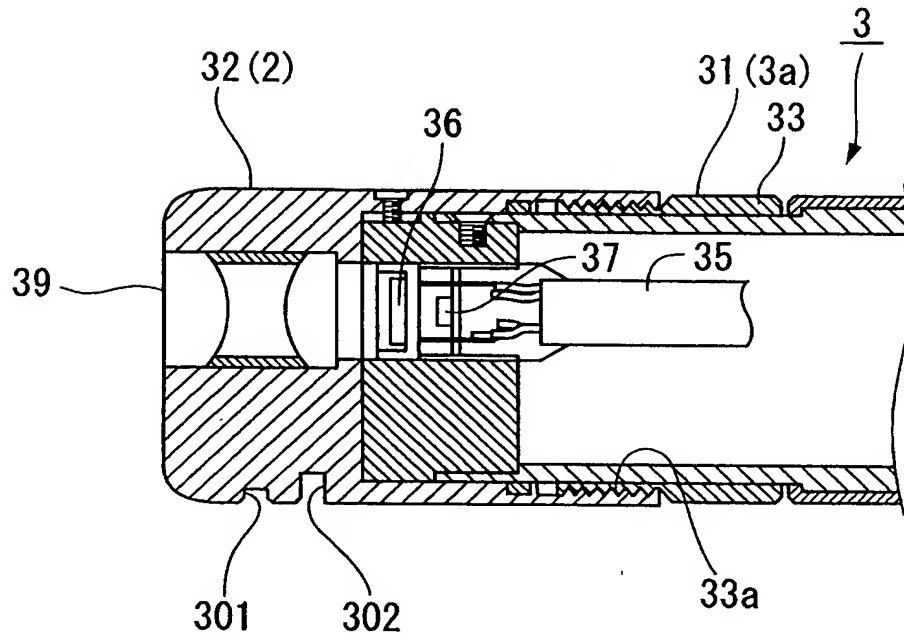


FIG. 38

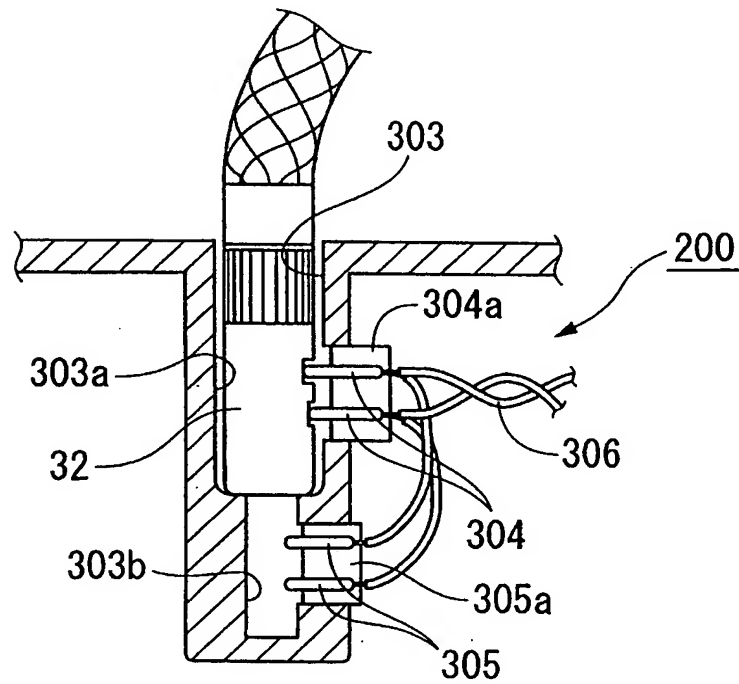


FIG. 39

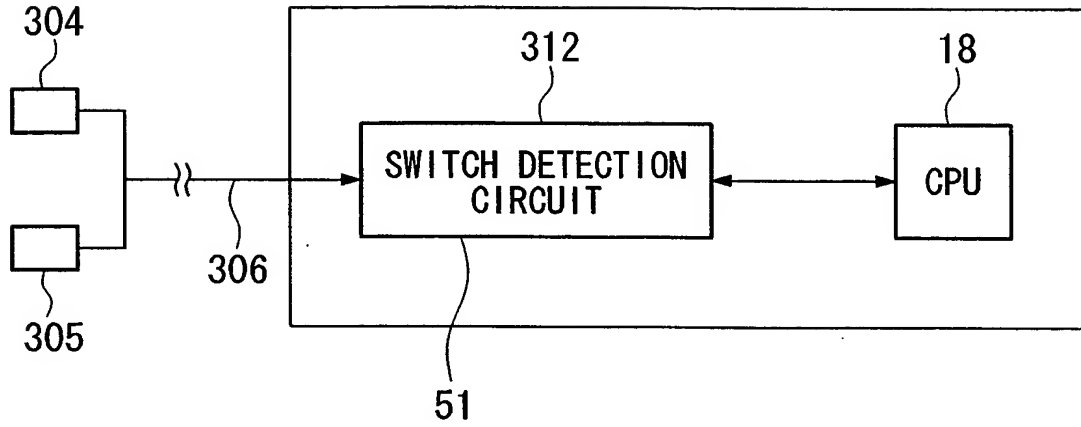


FIG. 40

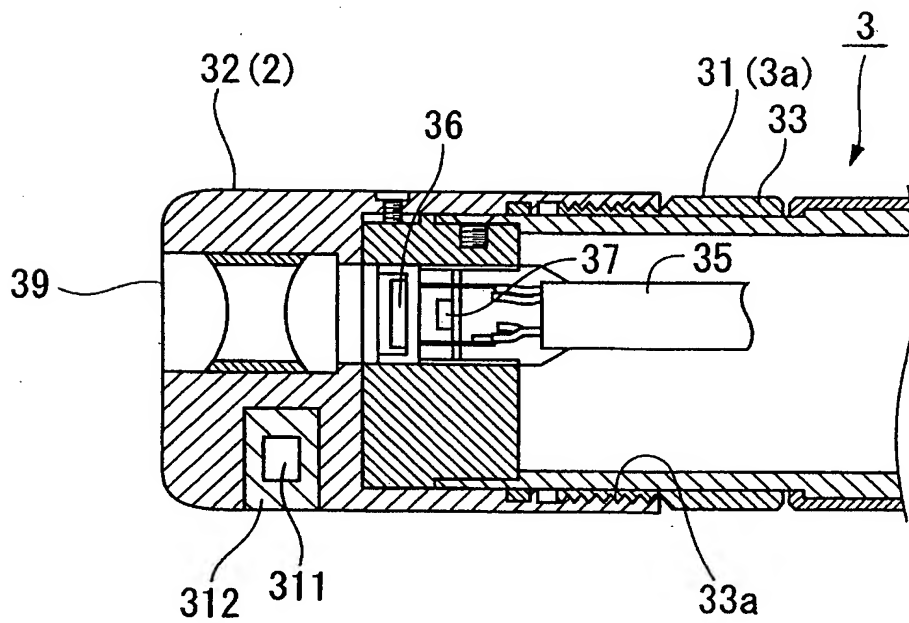


FIG. 41

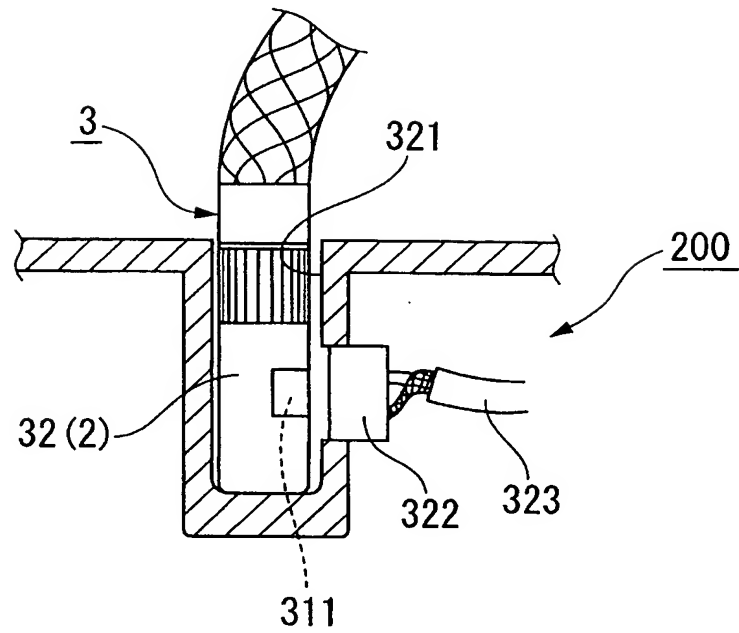


FIG. 42

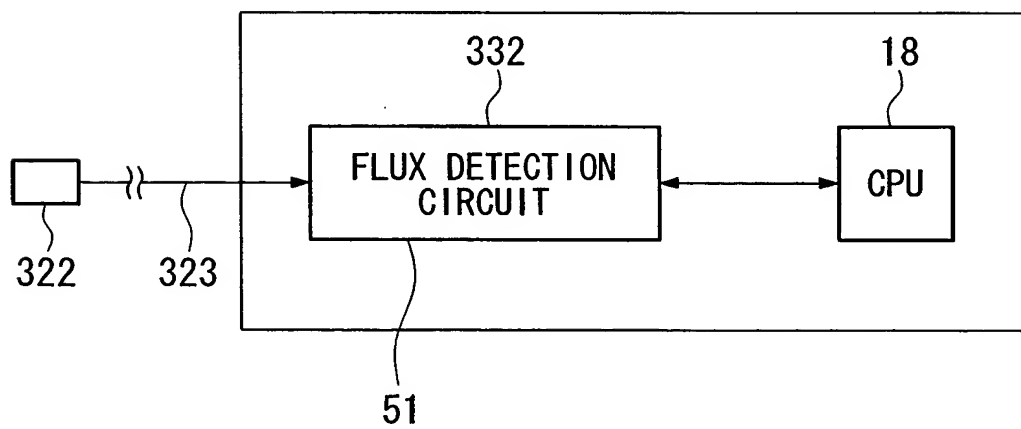


FIG. 43

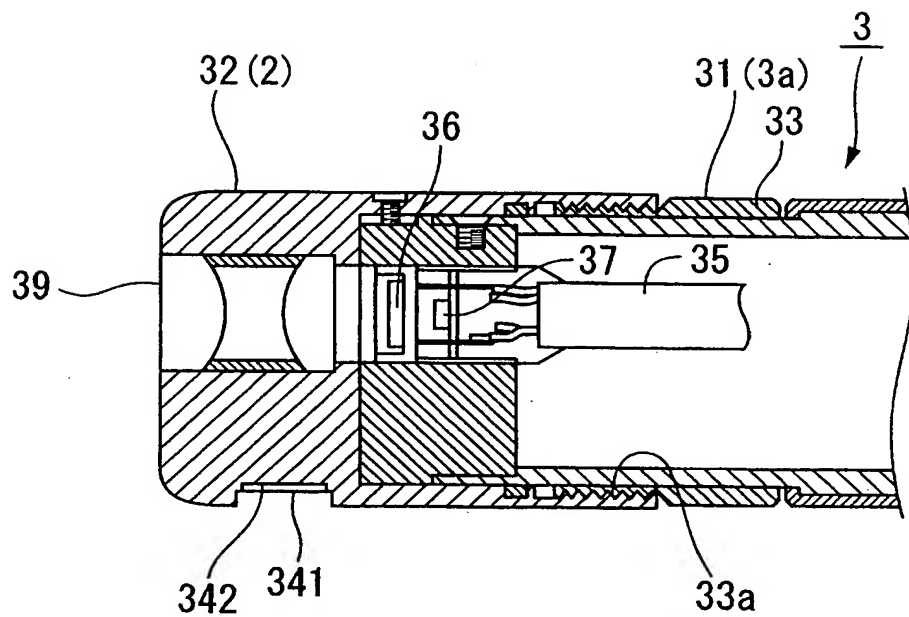


FIG. 44

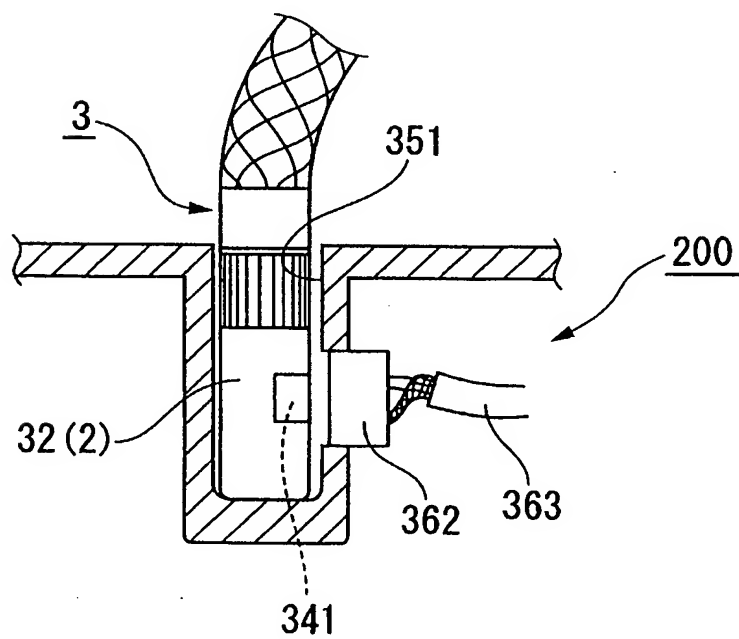




FIG. 45

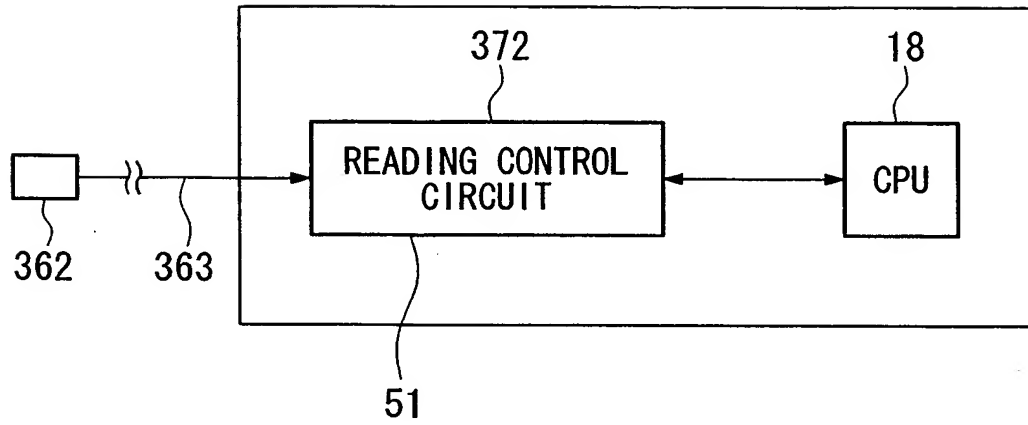


FIG. 46

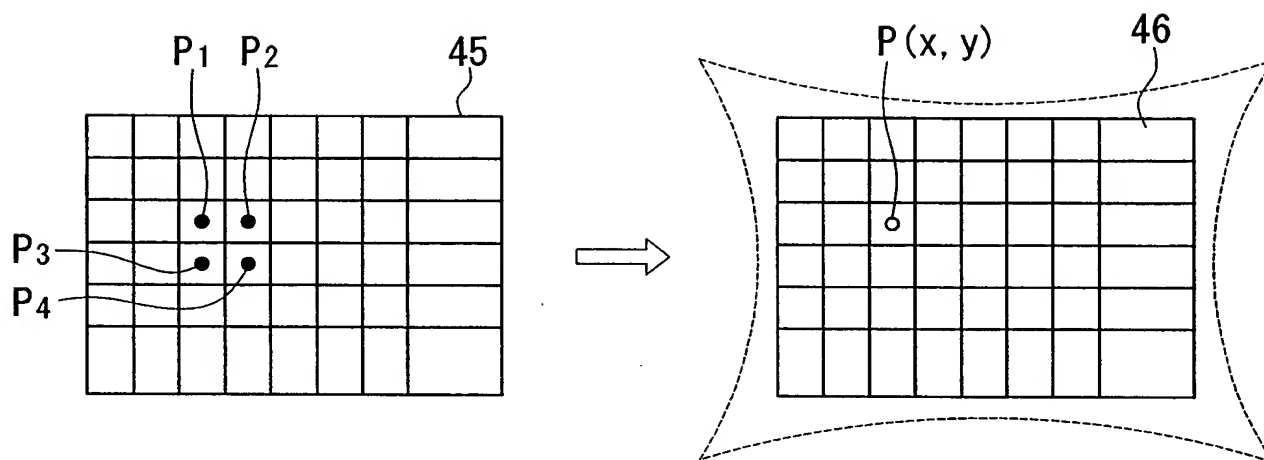


FIG. 47

